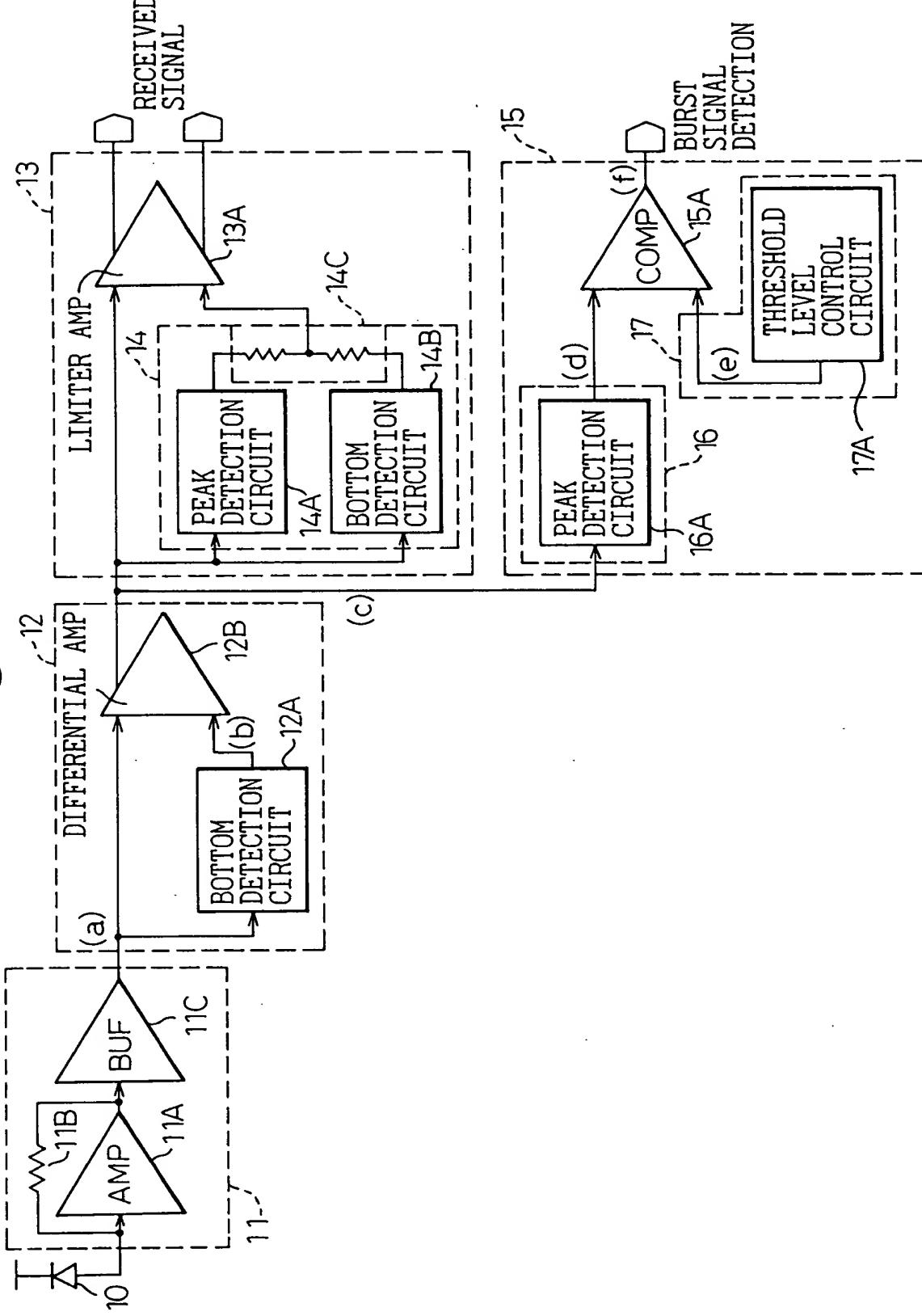


Fig.1



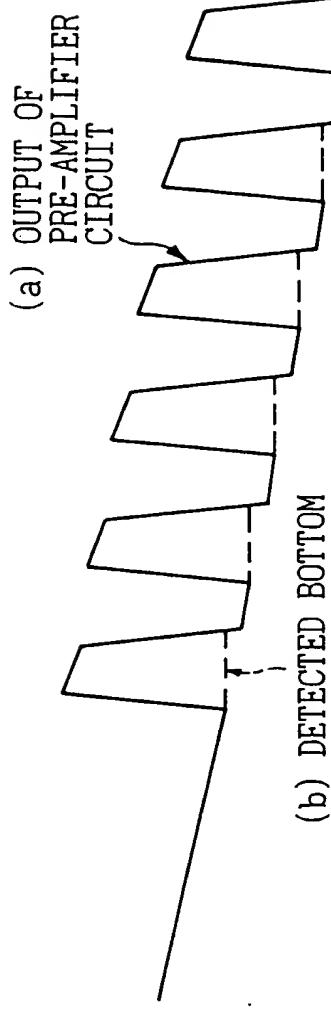


Fig. 2A WAVEFORM IN CIRCUIT 12.

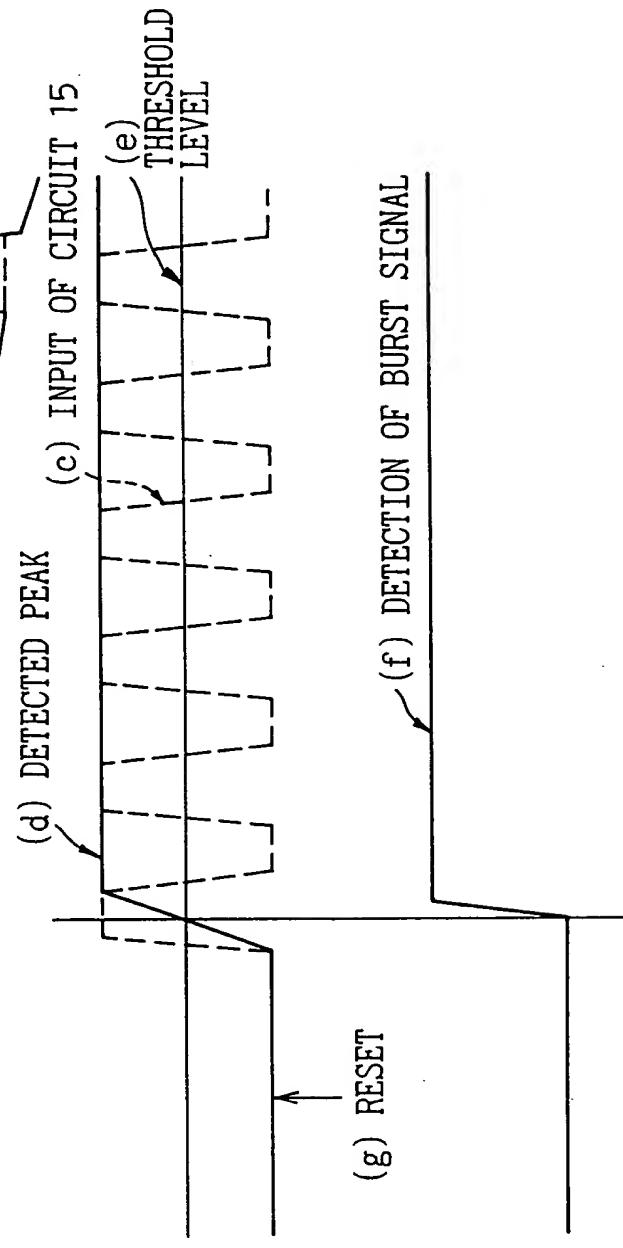


Fig. 2B WAVEFORM IN CIRCUIT 15

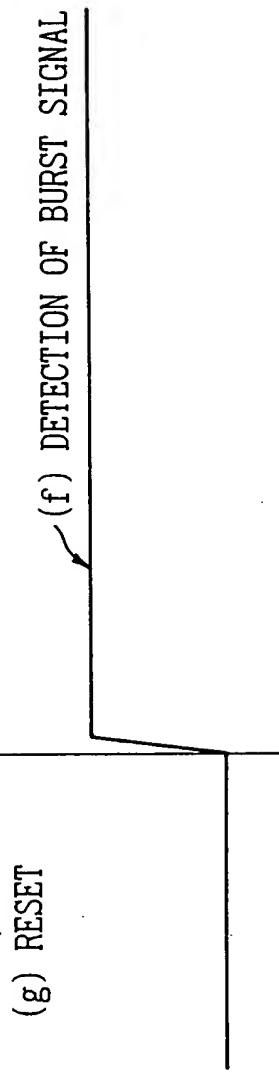


Fig. 2C OUTPUT OF COMP 15A

Fig.3A

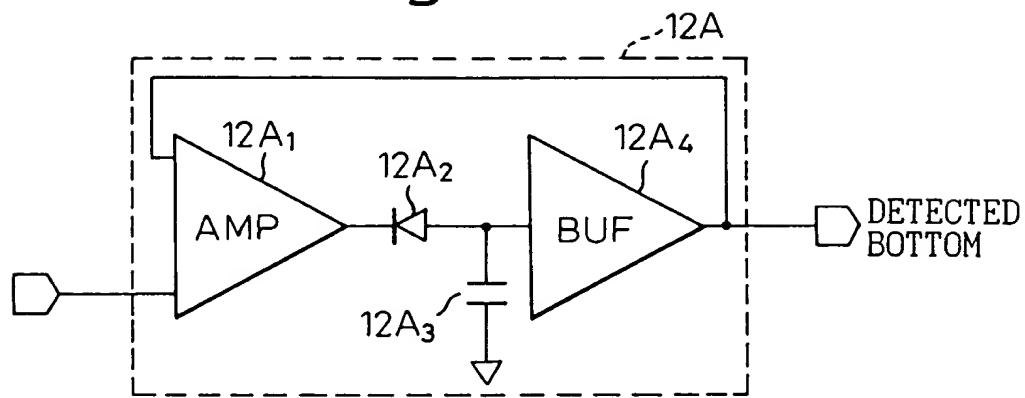


Fig.3B

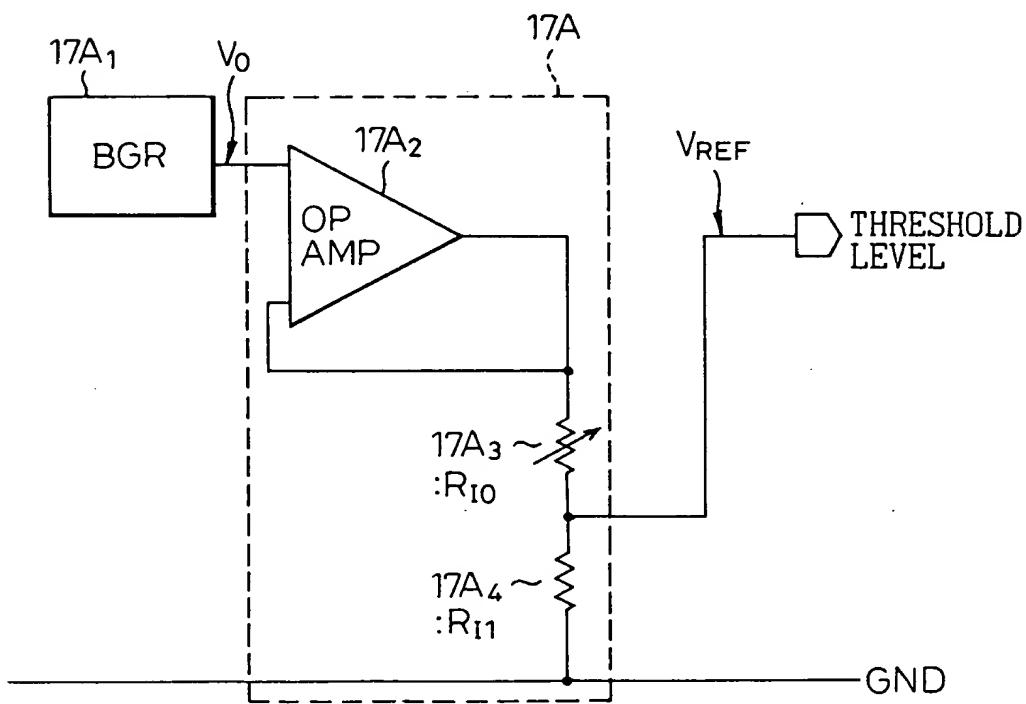


Fig. 4

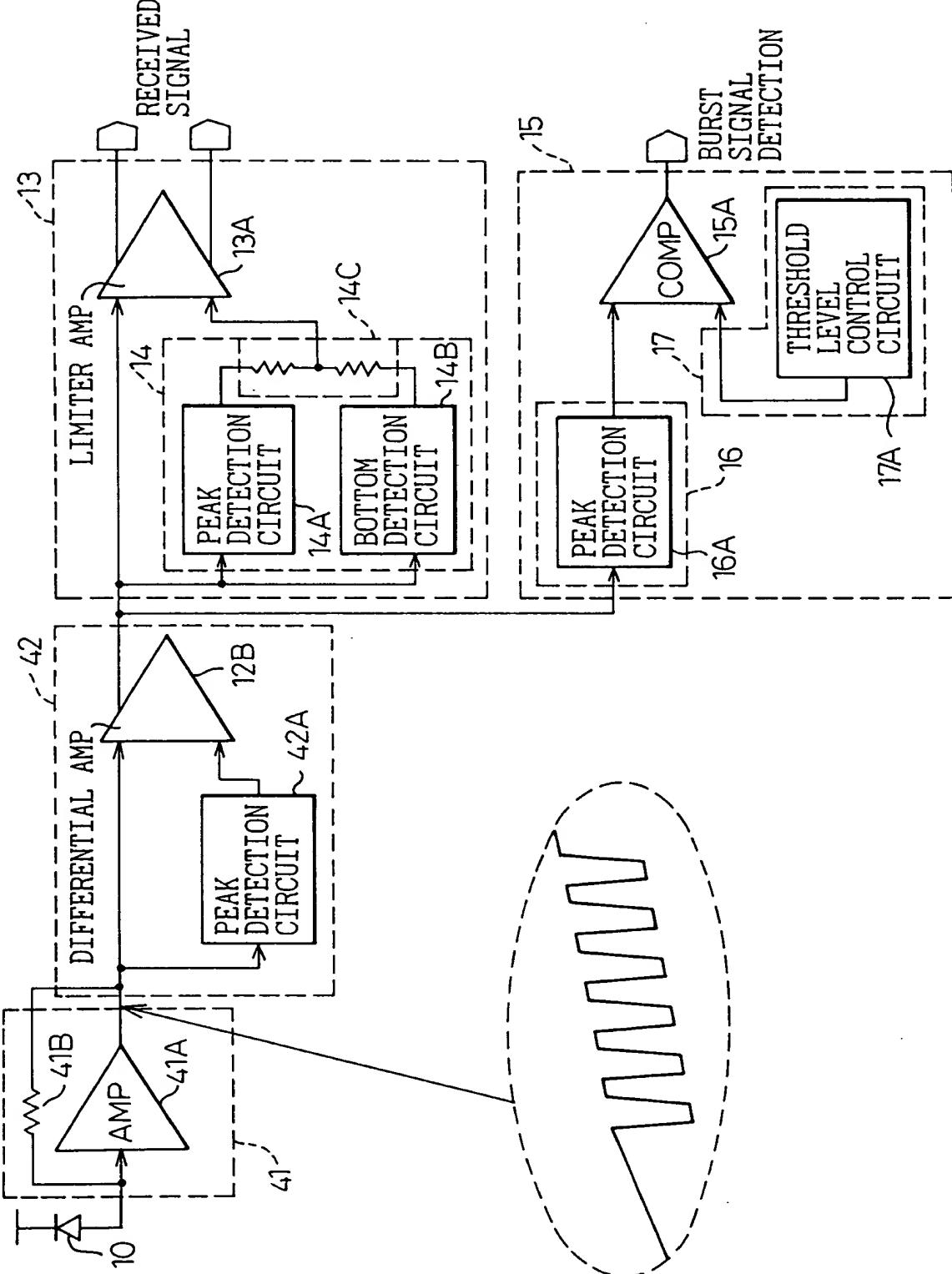
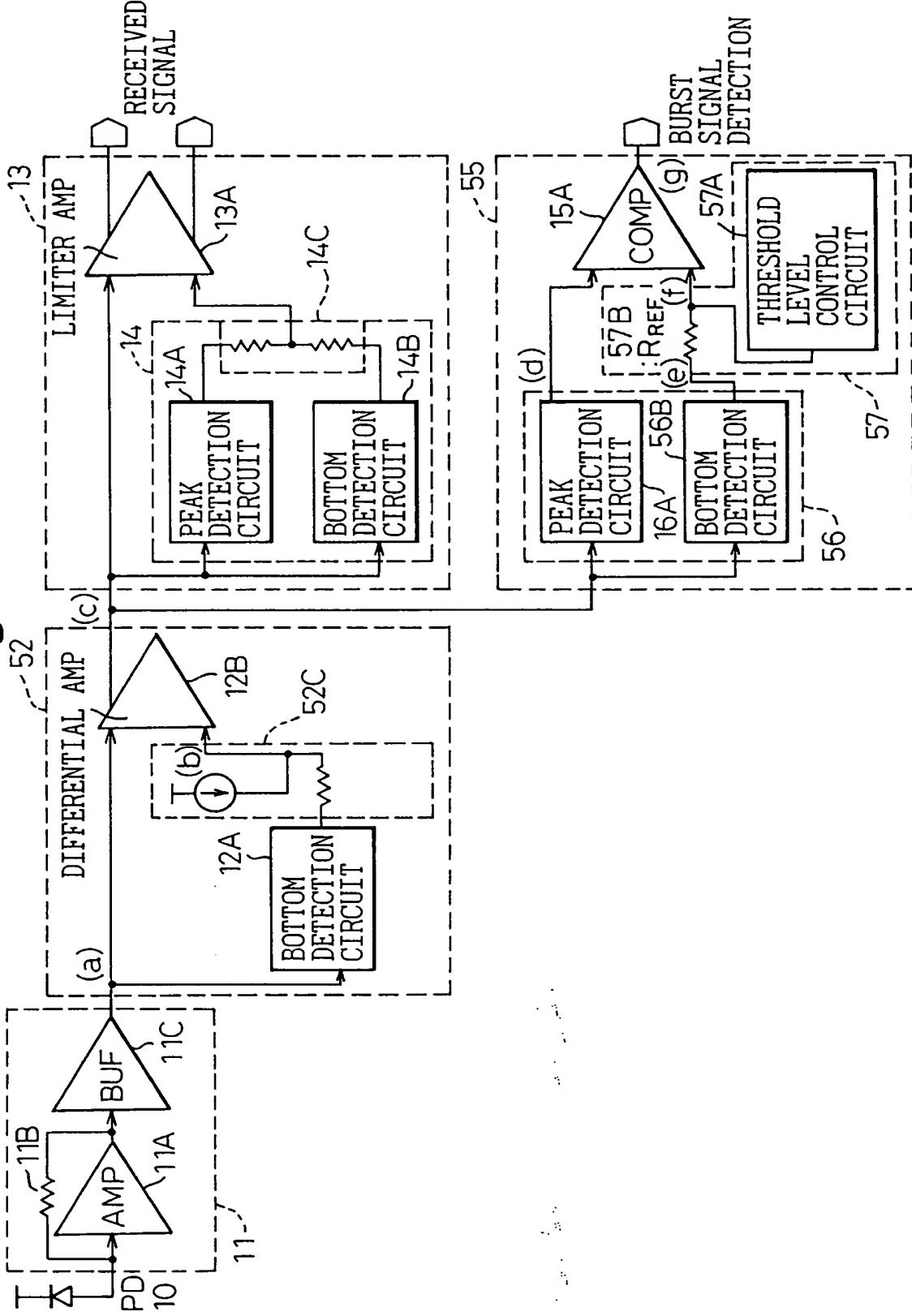


Fig. 5



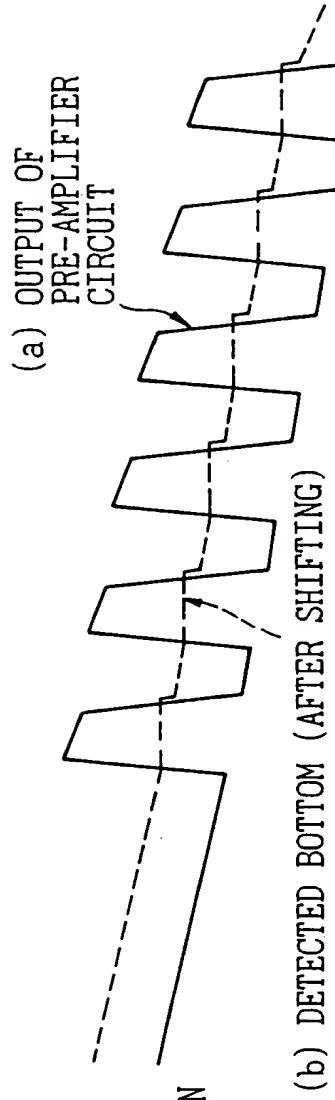


Fig. 6A WAVEFORM IN CIRCUIT 52

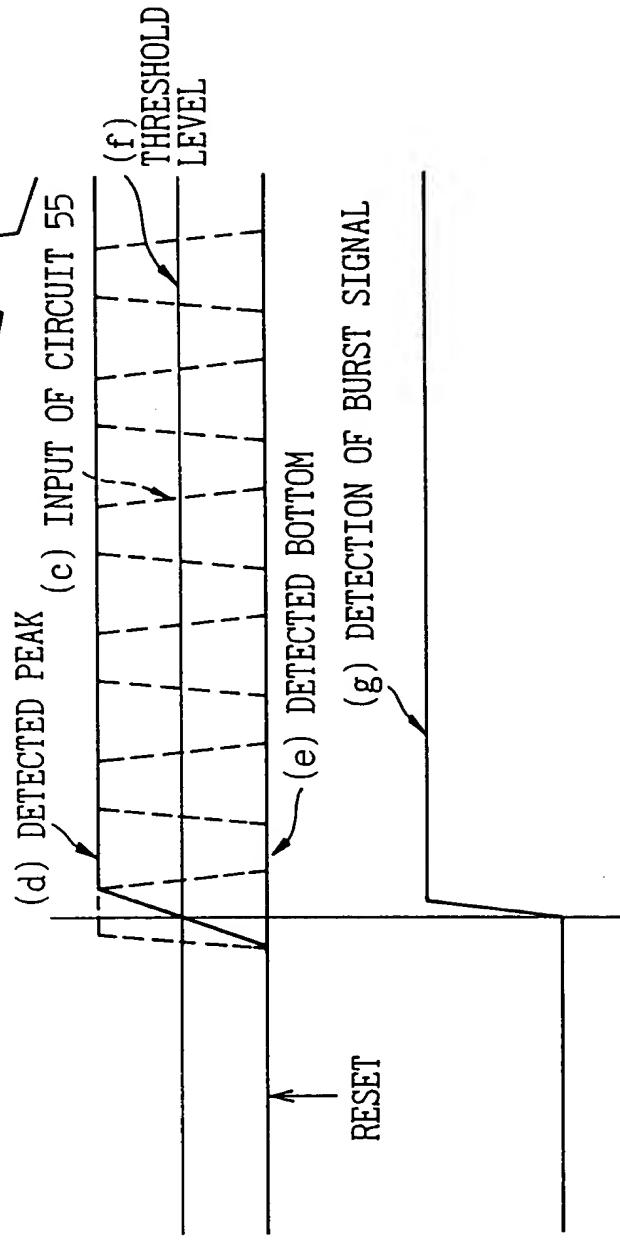


Fig. 6B WAVEFORM IN CIRCUIT 55

Fig. 6C OUTPUT OF COMP 15A

7/23

Fig.7

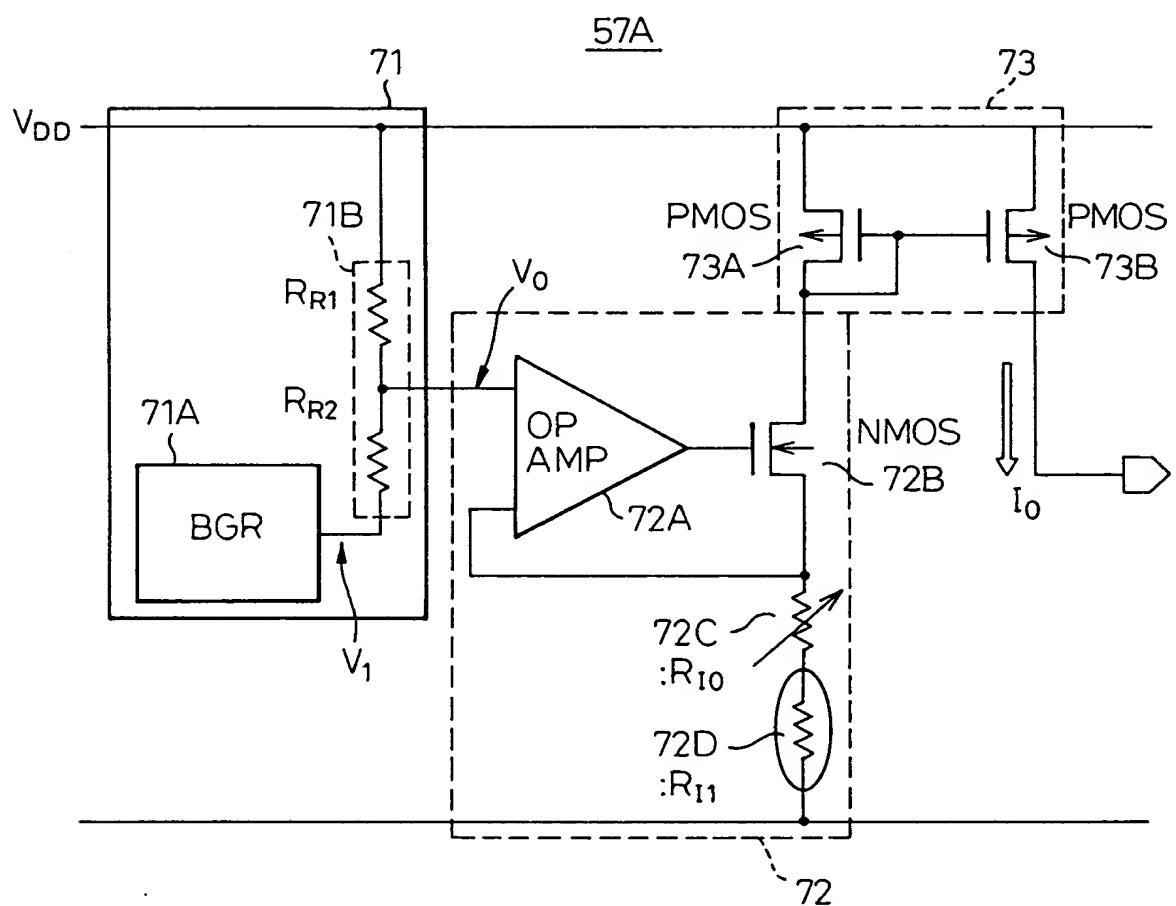
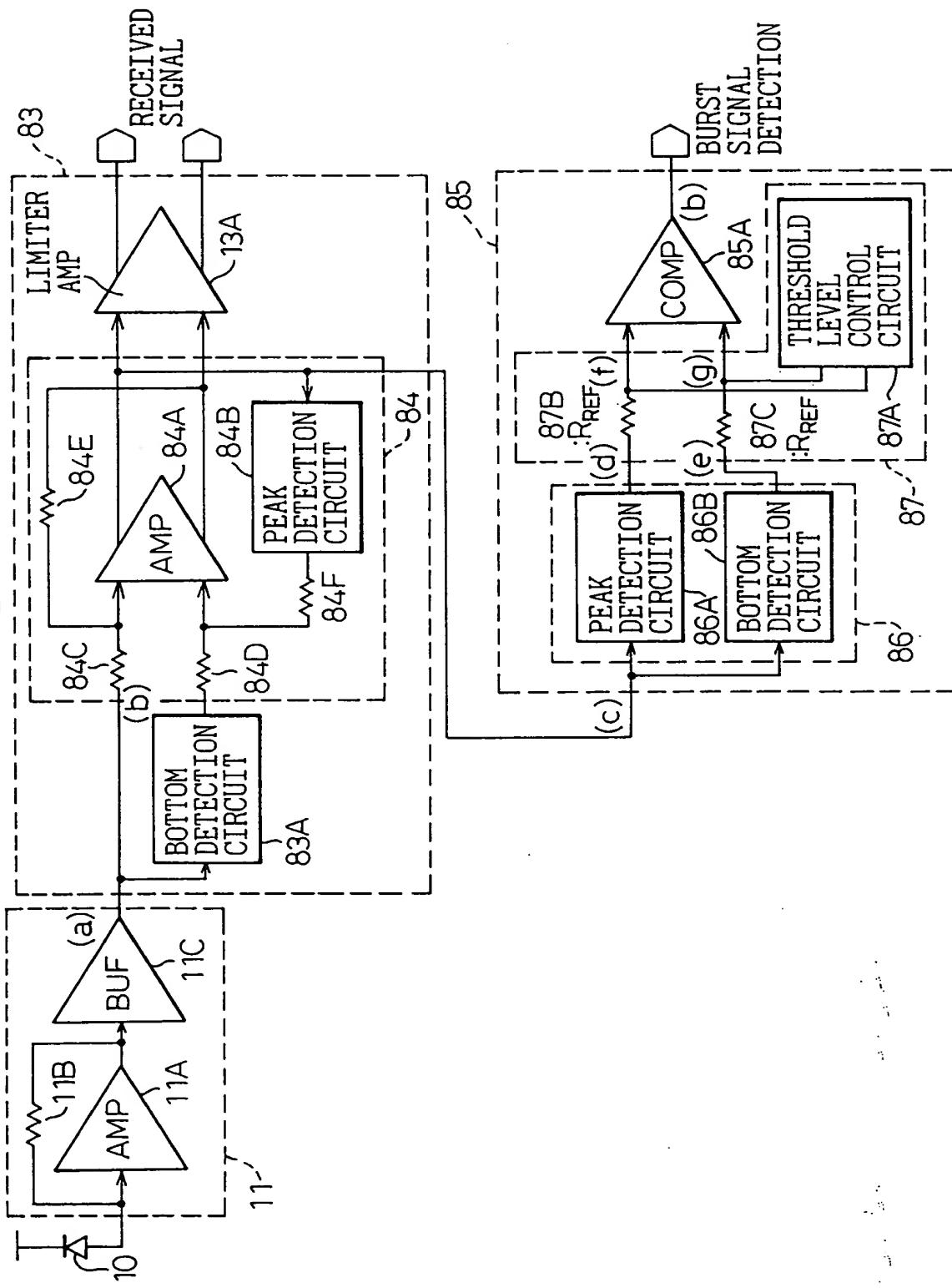


Fig.8



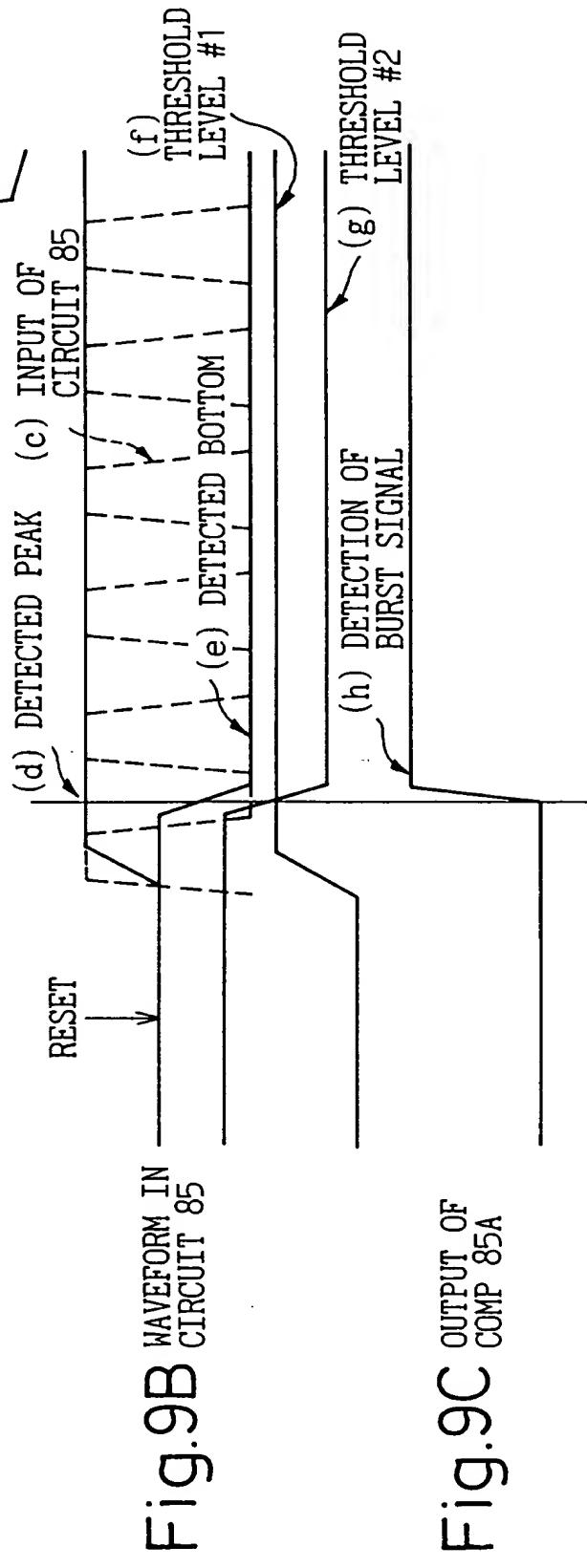
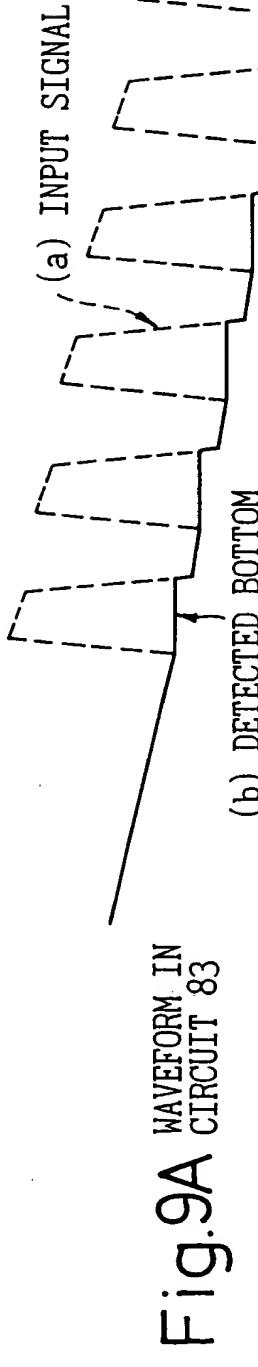


Fig.9C OUTPUT OF COMP 85A

10  
23

Fig.10A

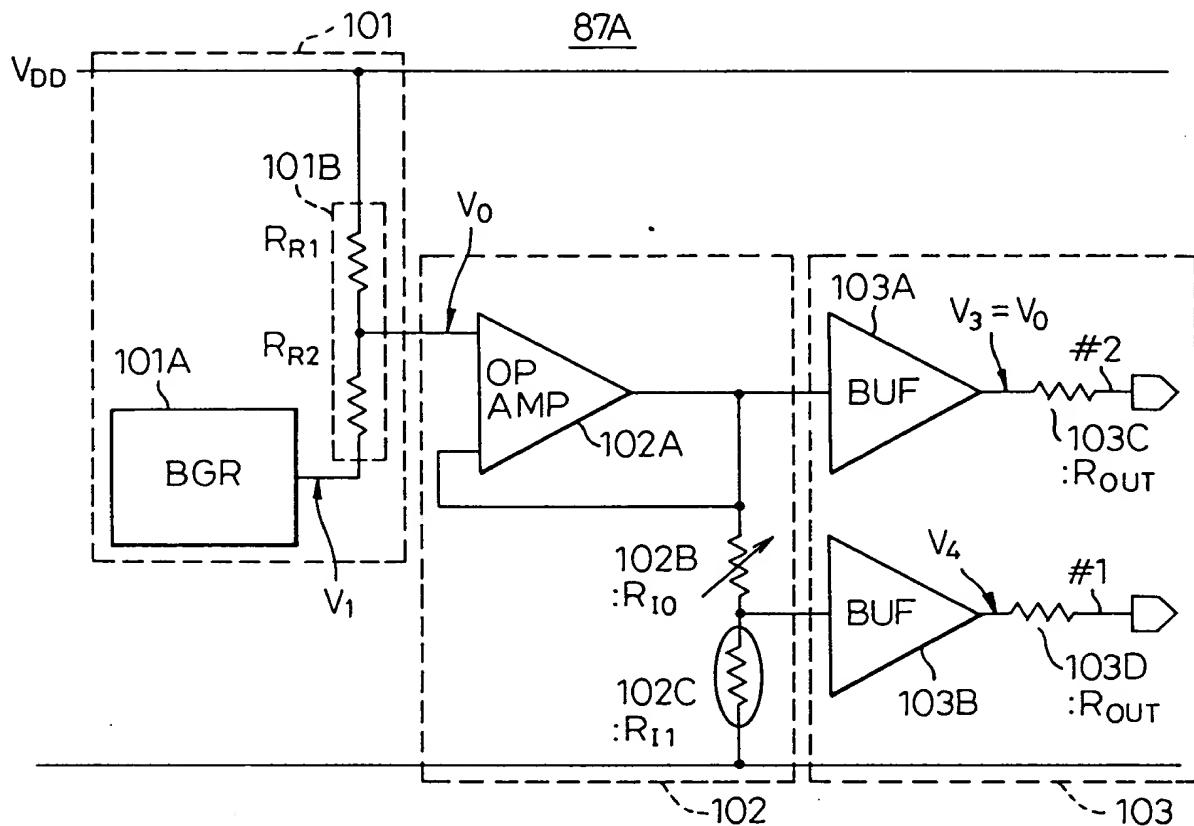


Fig.10B

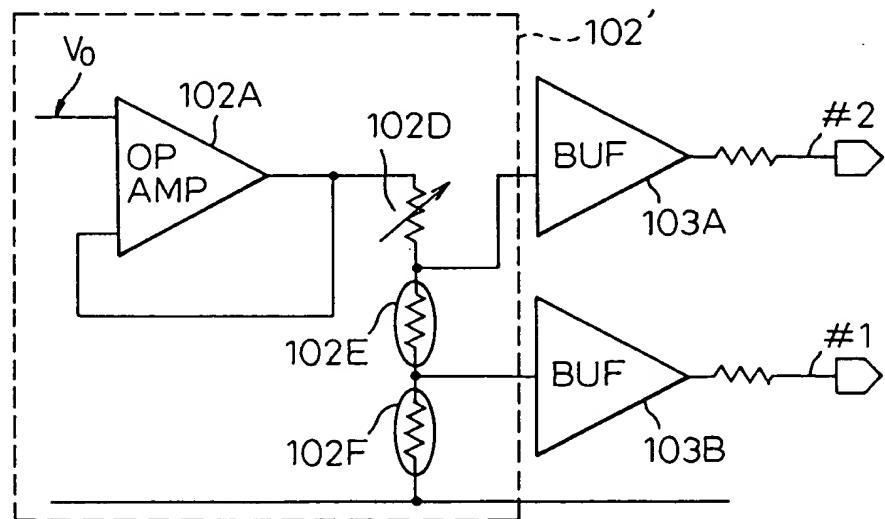
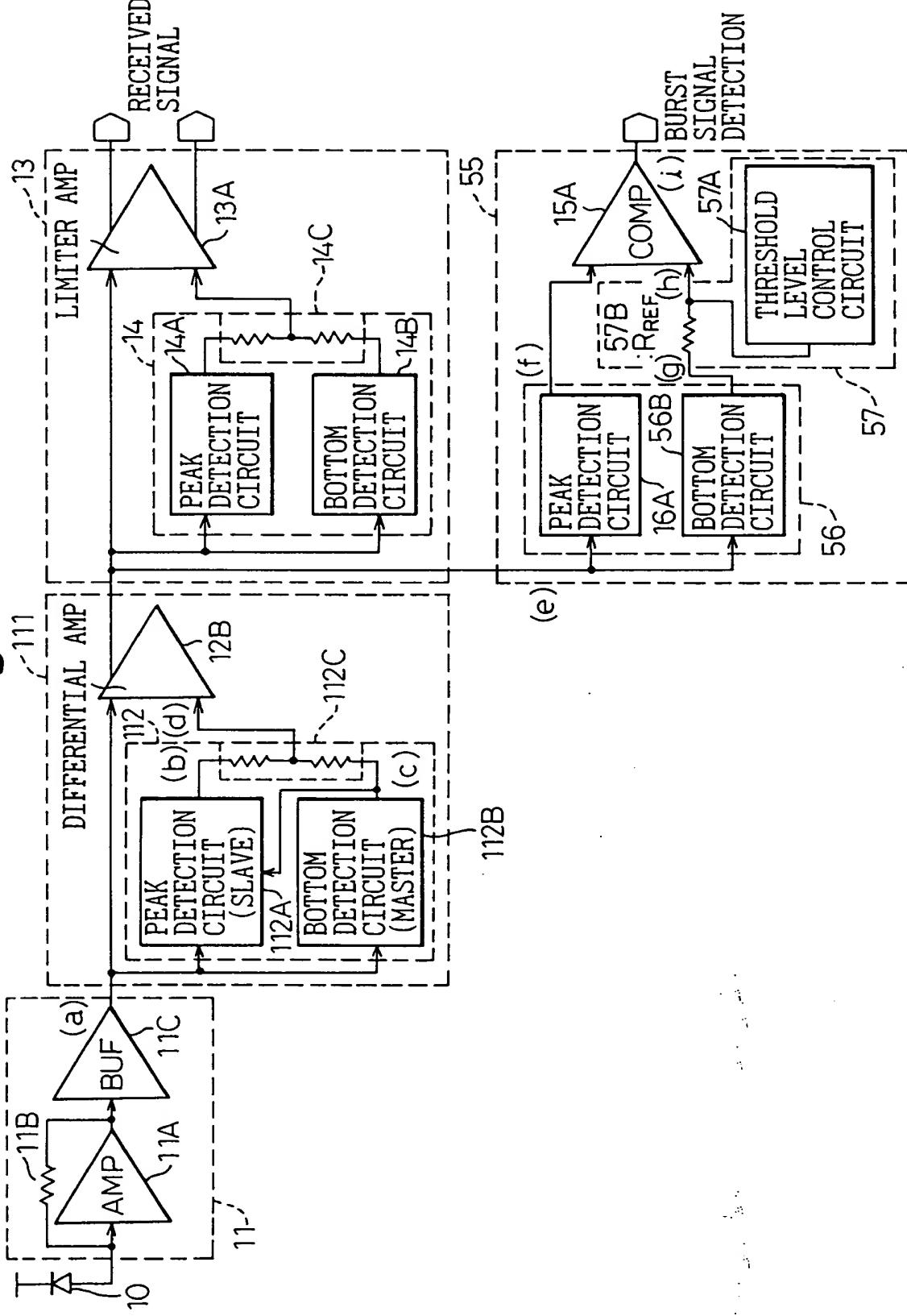


Fig.11



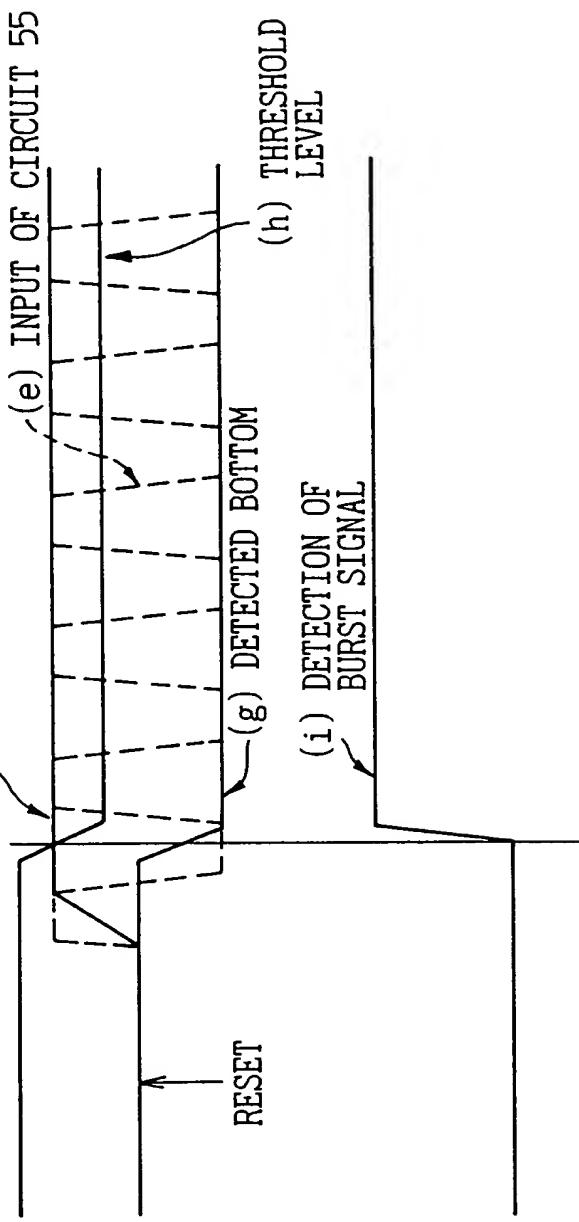
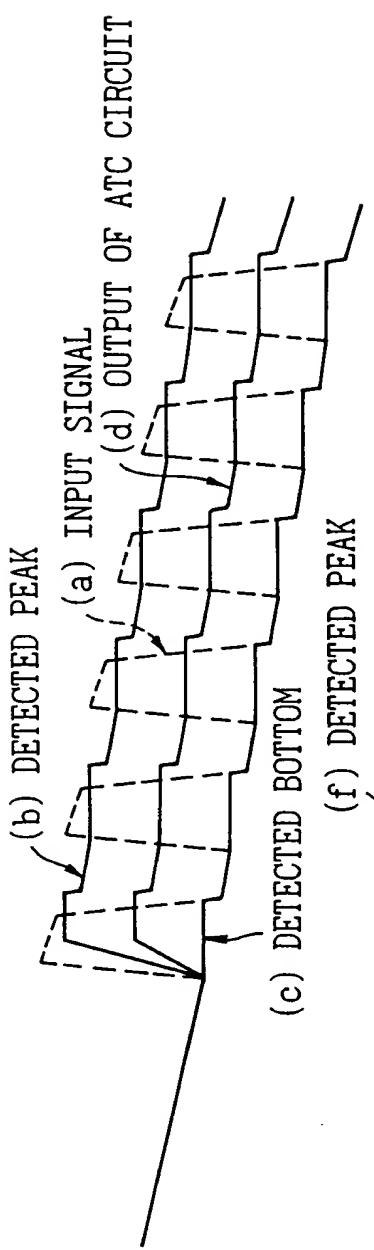


Fig.12C OUTPUT OF COMP 15A

Fig.13

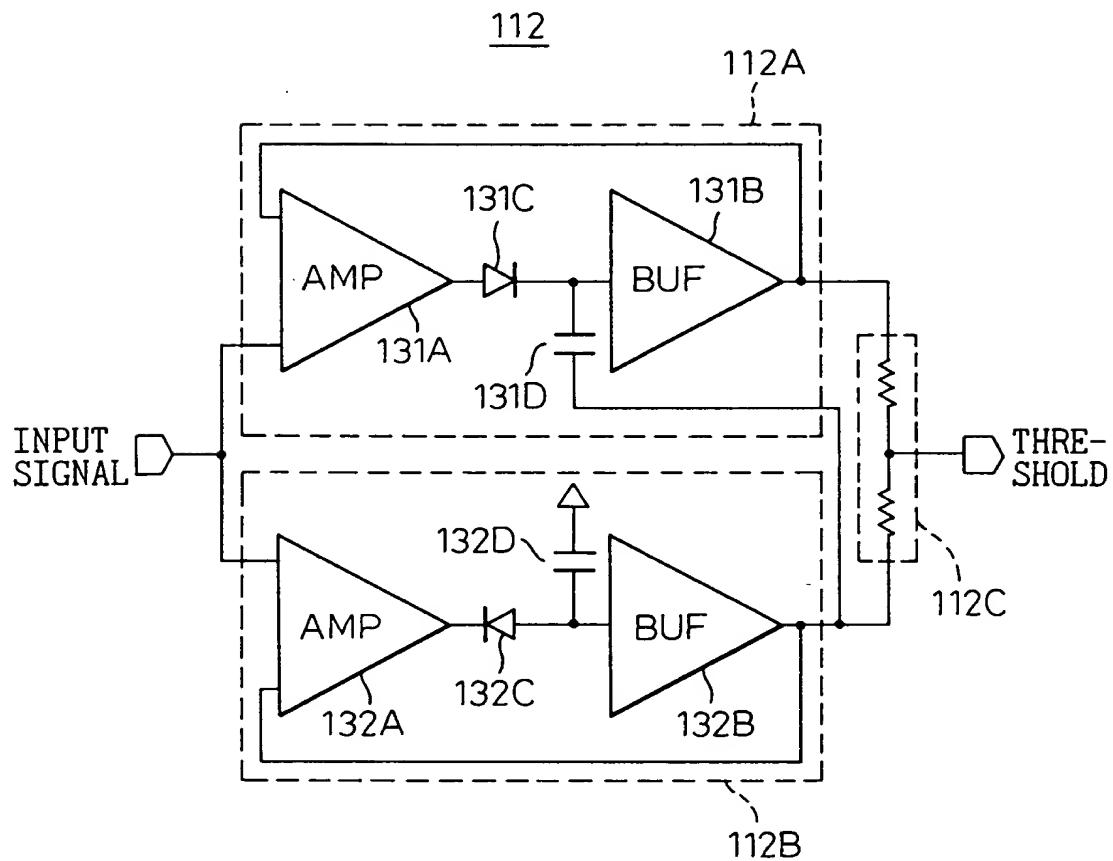


Fig. 14

14/23

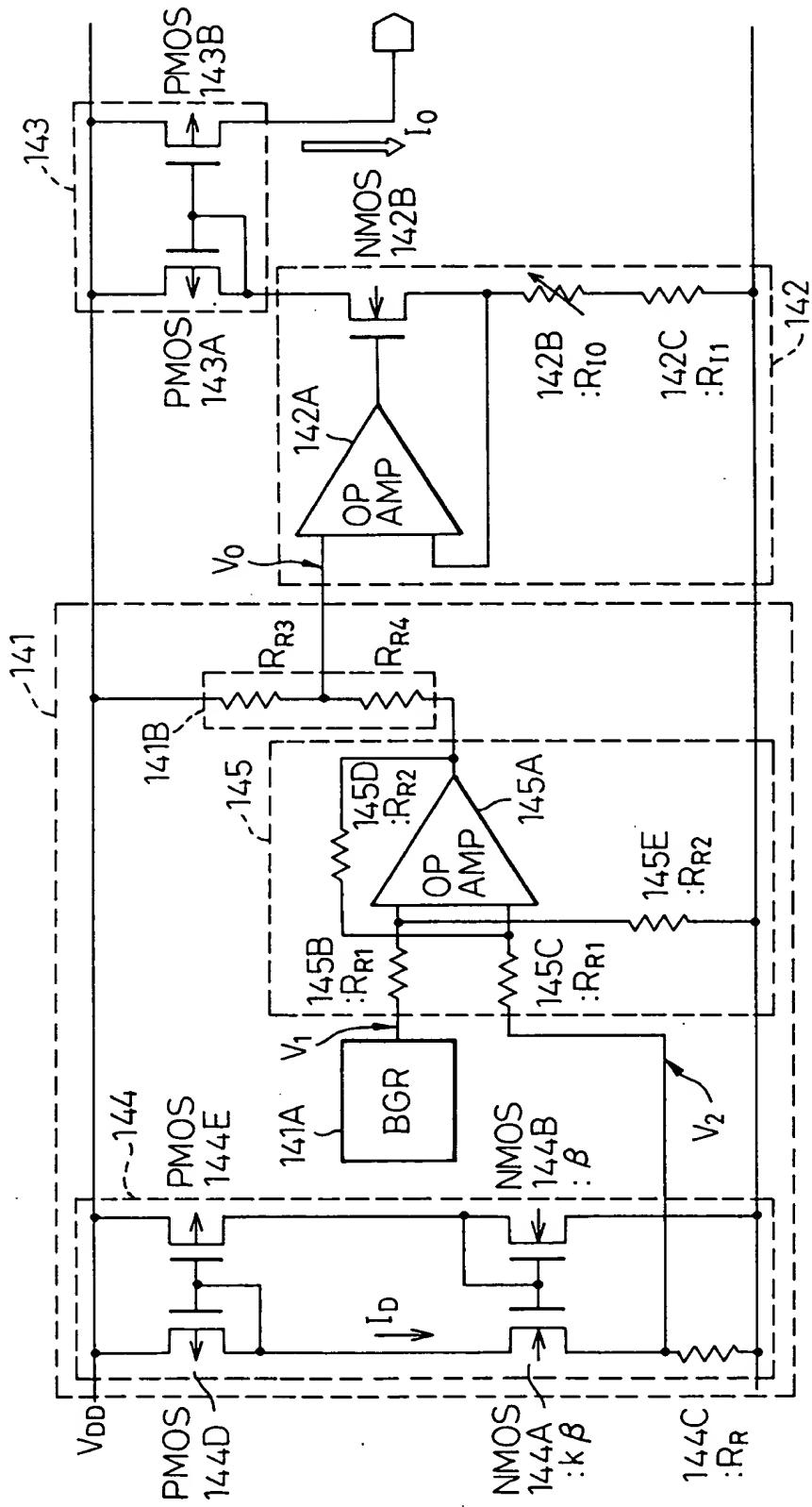
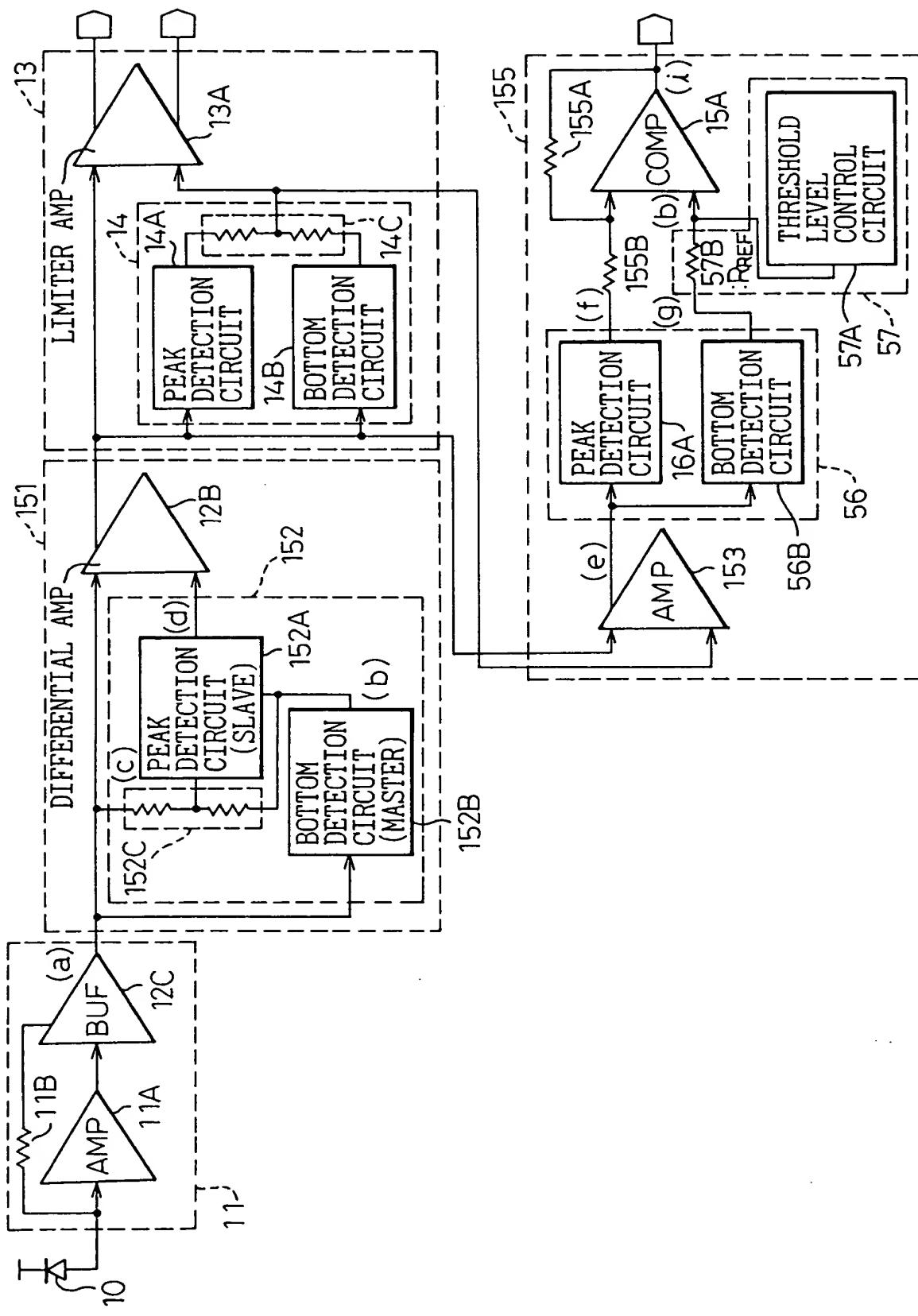


Fig. 15



(b) DIVIDED VOLTAGE SIGNAL

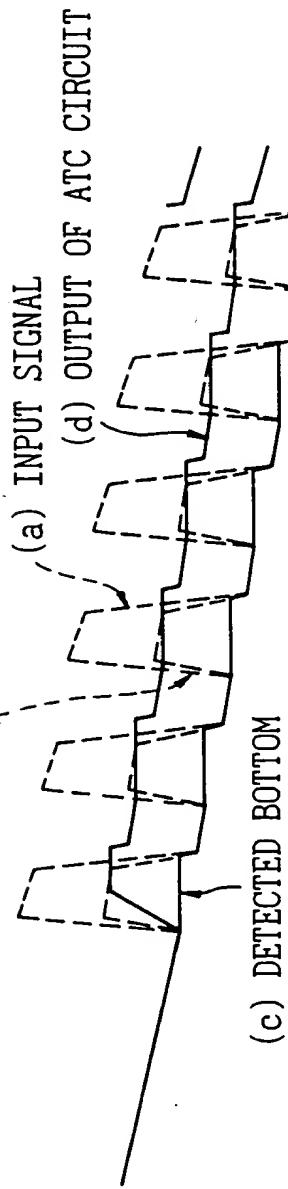


Fig.16A  
WAVEFORM IN  
CIRCUIT 151

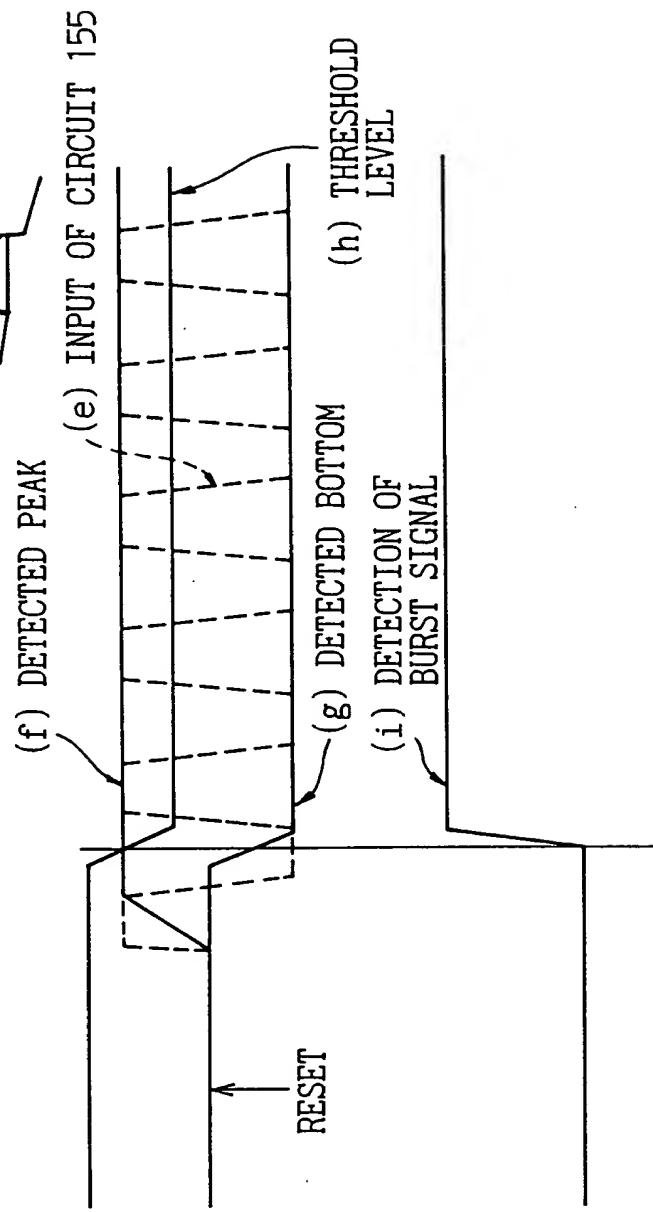
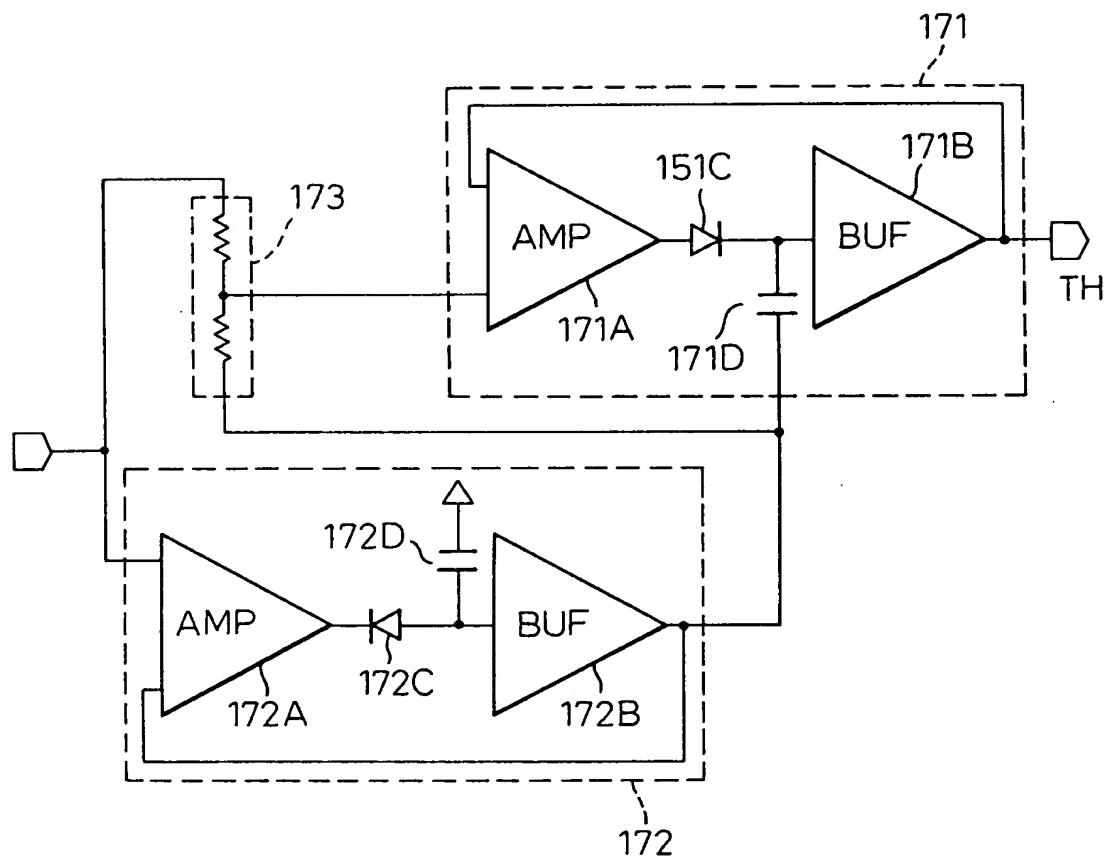


Fig.16B  
WAVEFORM IN  
CIRCUIT 155

Fig.16C  
OUTPUT OF  
COMP 15A

Fig.17



18  
FEB

18/23

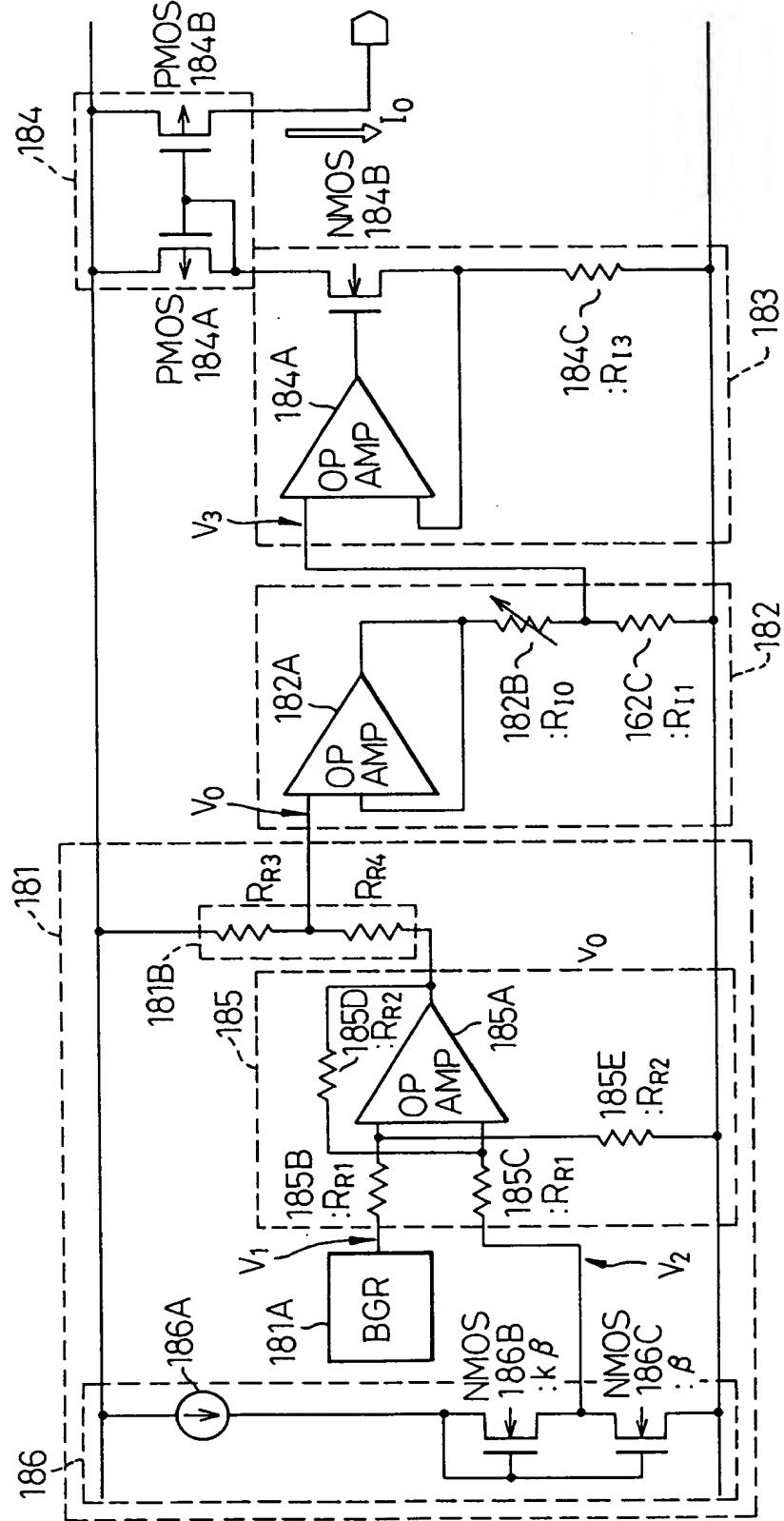
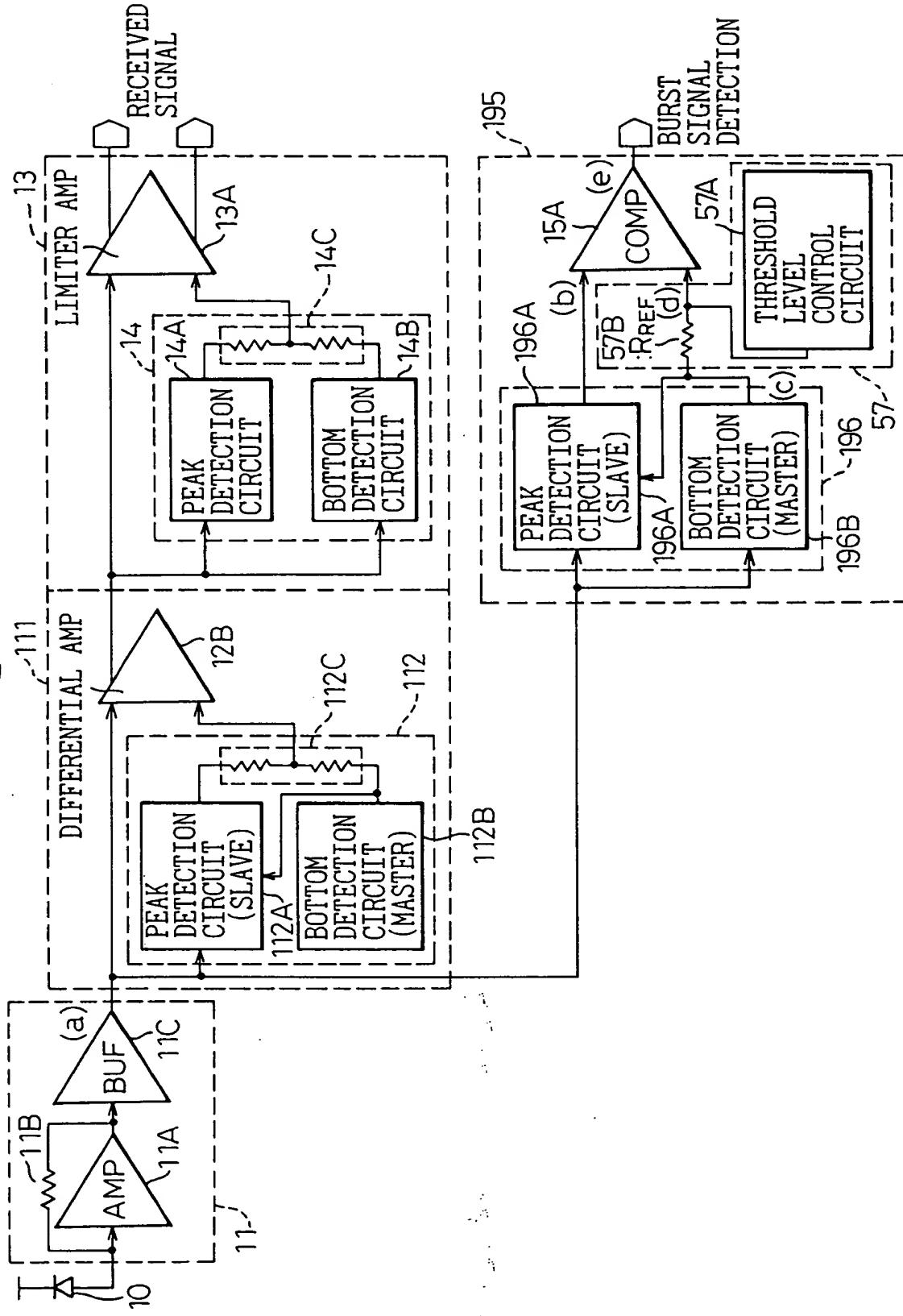


Fig. 19



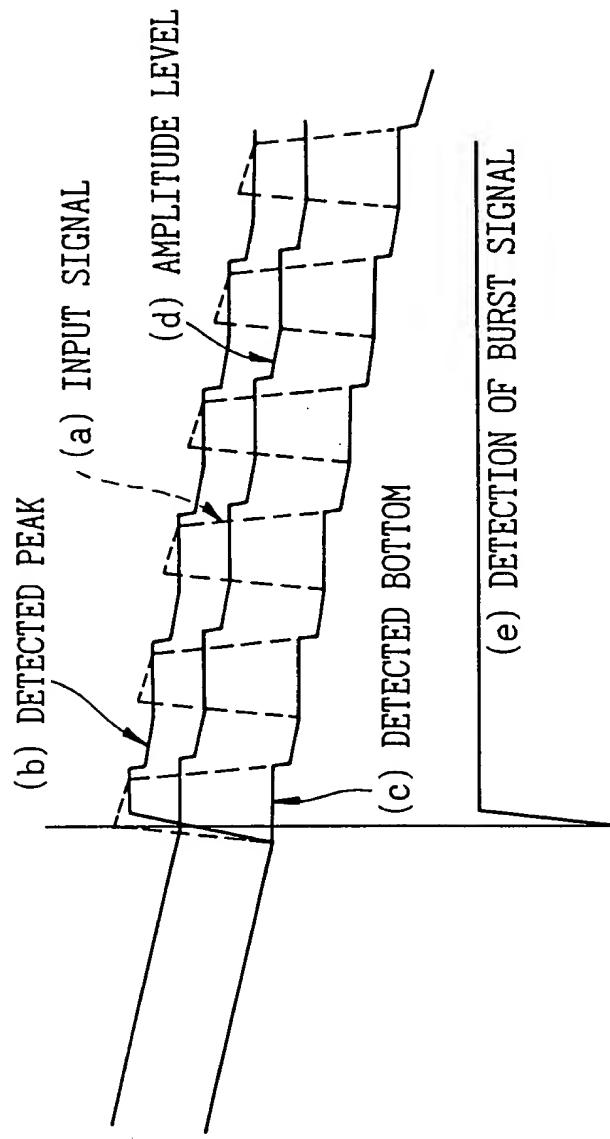
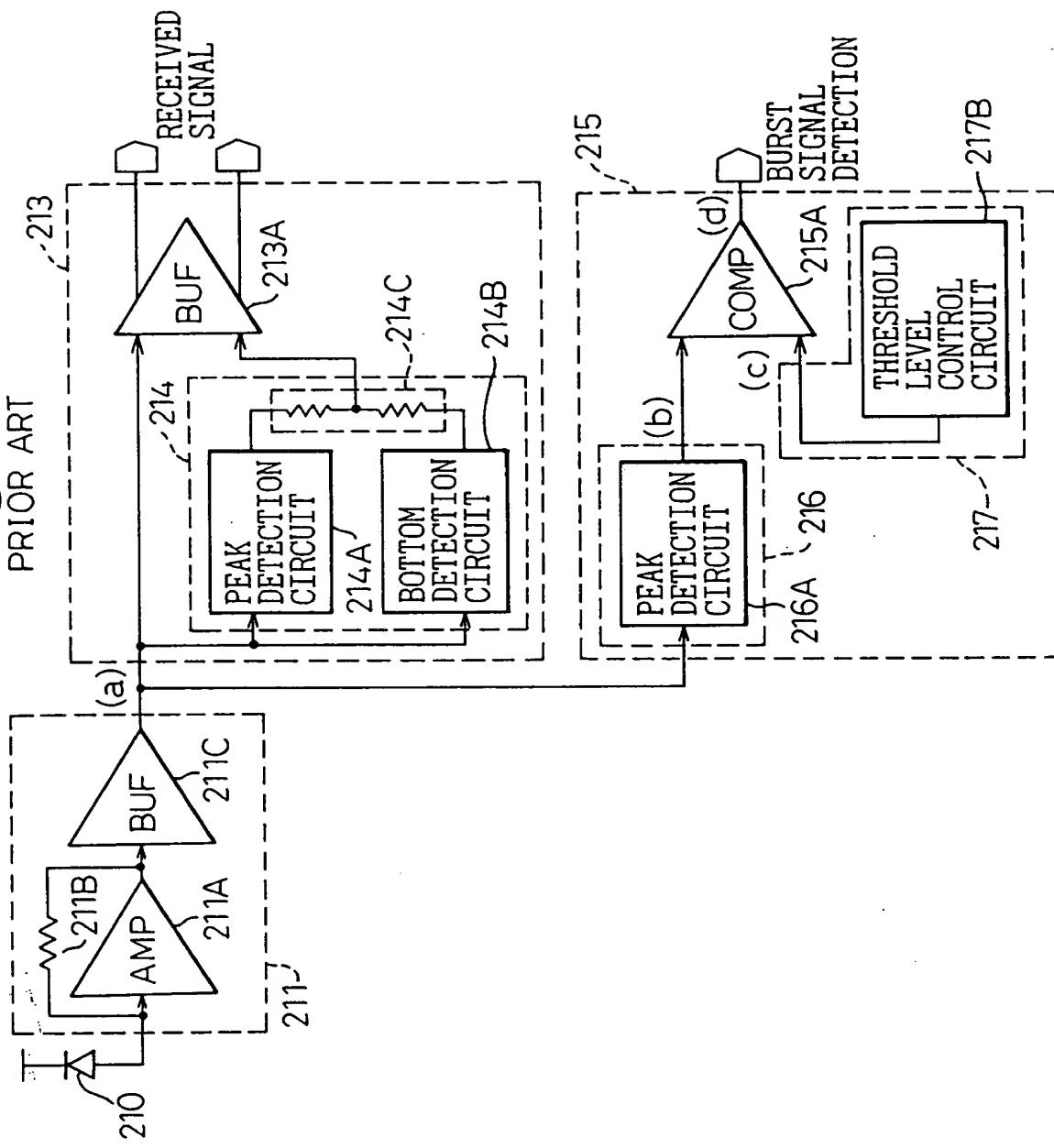


Fig.20A WAVEFORM IN CIRCUIT 195

Fig. 21



22/23

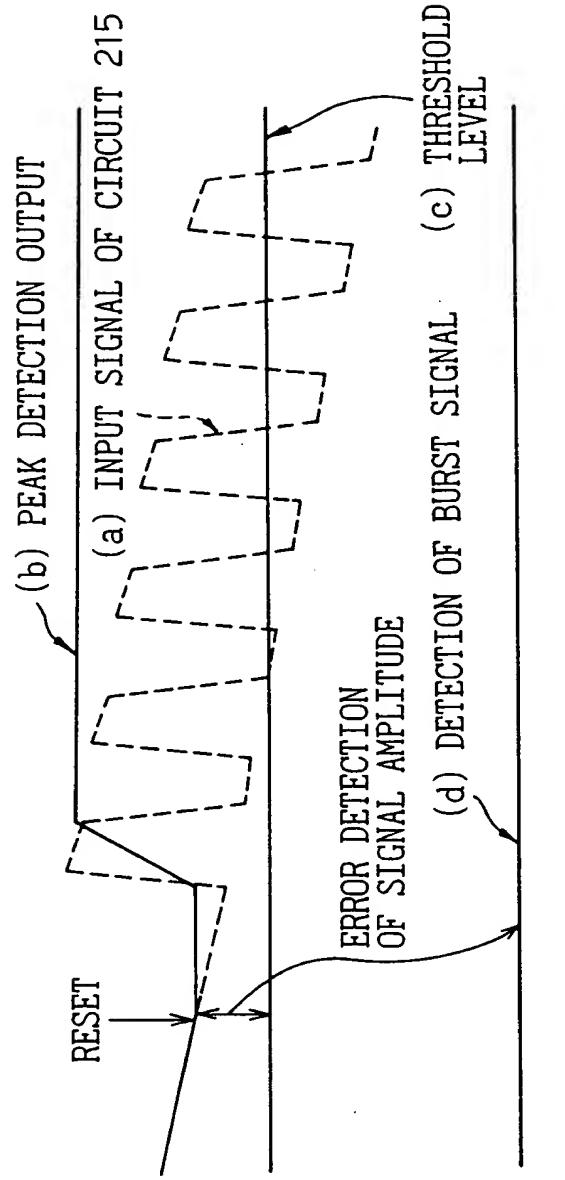


Fig.22A  
WAVEFORM IN  
CIRCUIT 215  
PRIOR ART

Fig.22B  
OUTPUT OF  
COMP 215A  
PRIOR ART

23/23

Fig.23A

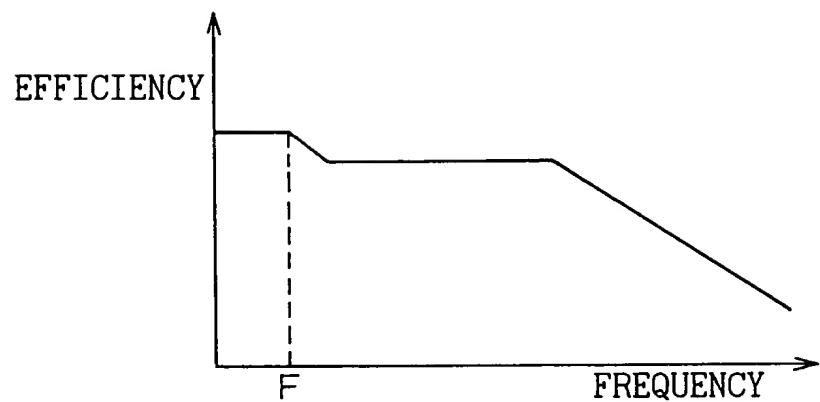


Fig.23B

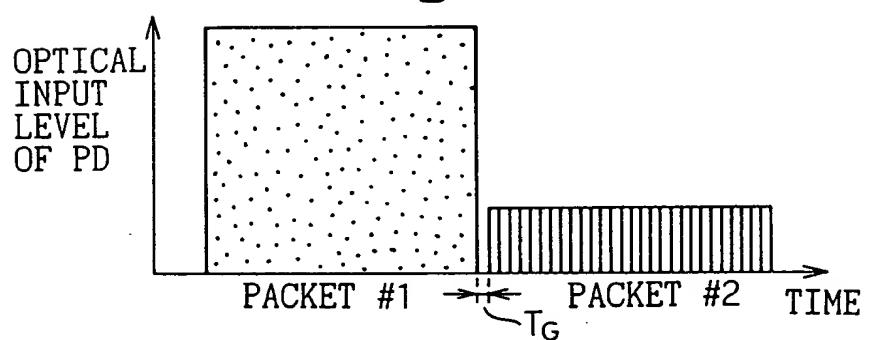


Fig.23C

